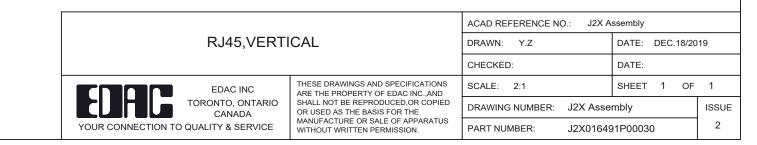
ISSUE NUMBER THIS IS A C.A.D. GENERATED DRAWING TOLERANCE (1)ORIGINAL UNLESS OTHERWISE SPECIFIED DO NOT MAKE MANUAL REVISIONS TO MASTER. 2.54 WAS 2.74 -CB JUL,28,2020 (2) .X ±0.25 .XX ±0.13 .XXX ±0.05 11.00 0.90 Ct 2.04 54 \sim 40 $\phi \phi$ RECOMMENDED PCB LAYOUT S \oplus \oplus 54 N 1.02 NOTES: 1. MATERIALS: **INSULATOR:** PBT, BLACK, UL 94V-0 CONTACT: PHOSPHOR BRONZE, 50µ" GOLD PLATING 2. VOLTAGE RATING: **125 VAC RMS** 3. CURRENT RATING: 1.5 AMPS @25°C 4. CONTACT RESISTANCE: $20m\Omega$ MAX 8 5. INSULATOR RESISTANCE: 1000 MO MIN @ 500V DC 5. DIELECTRIC WITHSTANDING: 1000V AC CONTACT TO CONTACT OR Ó 1500VDC CONTACT TO SCREEN AND TEST PANEL @0.5mA 50Hz OR 60Hz 1 MIN 750 CYCLES MIN 6. DURABILITY: 7. MATING/UNMATING FORCE: 30N/3.06KG MAX 8. RETENTION STRENGTH: 50N MIN FOR 60 S +15 S 9. OPERATING TEMP: -40°C TO +85°C 1.02 TYP 10. STORAGE TEMP: -40°C TO +85°C RELATIVE HUMIDITY <80% 11. WAVE SOLDERING TEMP: 260+15°C FOR 5~10 SECONDS

MATES WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.





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