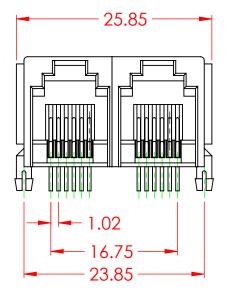
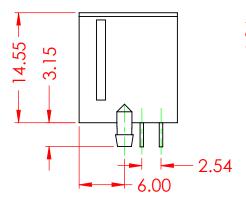
TOLERANCE UNLESS OTHERWISE SPECIFIED			
.X	±0.25		
.XX	±0.13		
.XXX	±0.05		

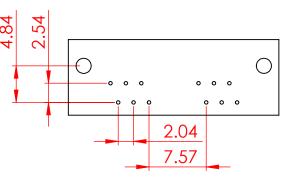
THIS IS A C.A.D. GENERATED DRAWING DO NOT MAKE MANUAL REVISIONS TO MASTER.



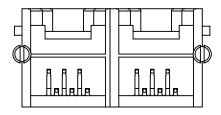
ISSUE NUMBER ORIGINAL







RECOMMENDED PCB LAYOUT



NOTES:

1. MATERIALS:

INSULATOR: PBT, BLACK, UL 94V-0

CONTACT: PHOSPHOR BRONZE, SELECT 50µ" GOLD PLATING ON CONTACT AREA

TIN ON TAILS OVER NICKEL





EDAC INC TORONTO, ONTARIO CANADA

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	ACAD REFERENCE NO.: J53 Assembly		
	DRAWN: Y.Z	DATE: DEC.20/20	19
	CHECKED:	DATE:	
	SCALE: 2:1	SHEET 1 OF	1
	DRAWING NUMBER: J53 Assembly		ISSUE

J53026692P00013

PART NUMBER:



THIS SERIES FULLY CONFORMS TO THE EUROPEAN UNION DIRECTIVES 2011/65/EU FOR RoHS COMPLIANCY.