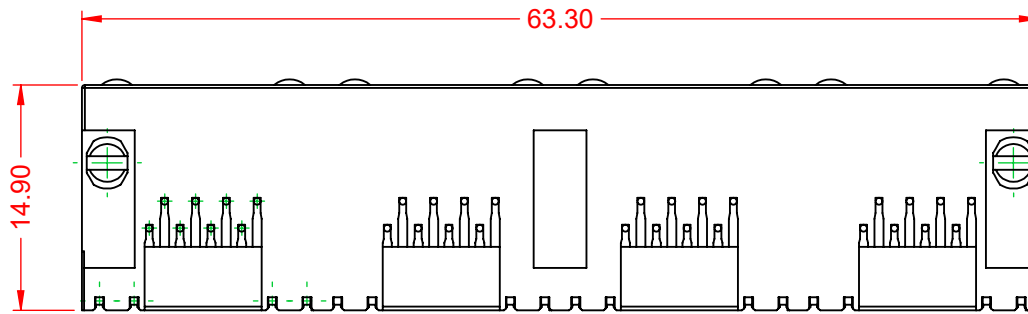
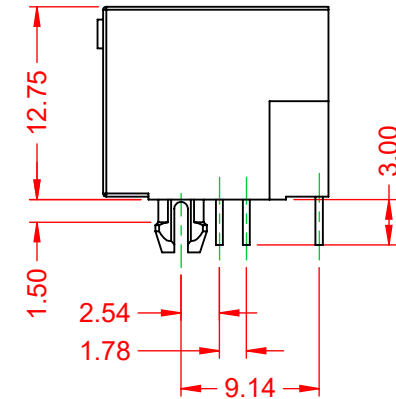
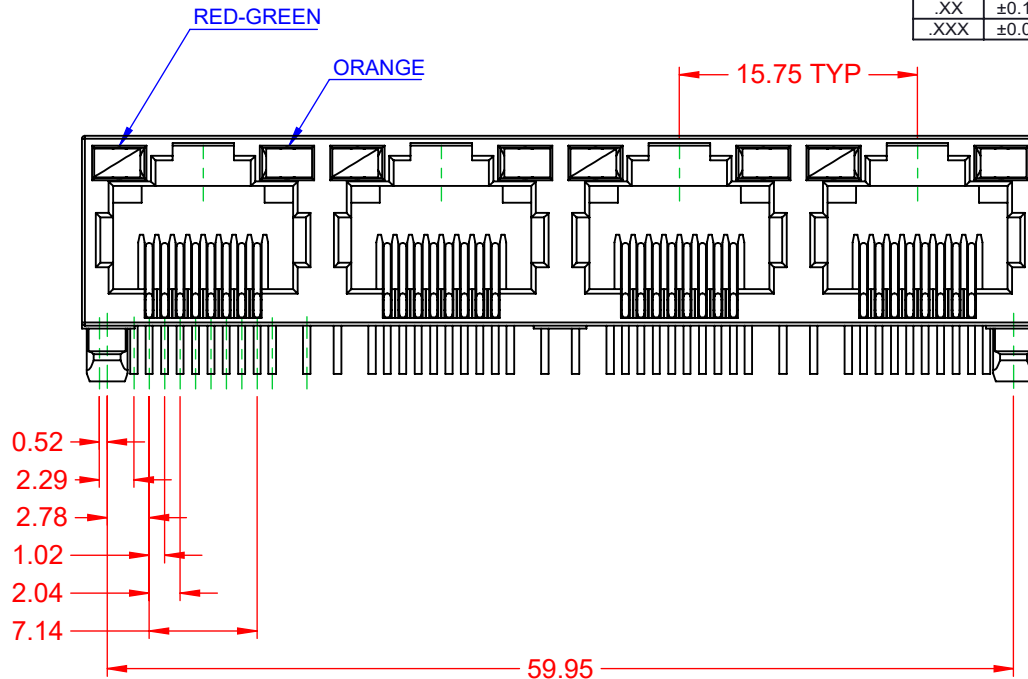


TOLERANCE UNLESS OTHERWISE SPECIFIED	
.X	±0.25
.XX	±0.13
.XXX	±0.05

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DO NOT MAKE MANUAL REVISIONS TO MASTER.



ISSUE NUMBER	
ORIGINAL	①



**NOTES:**

**1. MATERIALS:**

INSULATOR:  
CONTACT:

PBT, BLACK, UL 94V-0  
PHOSPHOR BRONZE,  
SELECT GOLD FLASH PLATING ON CONTACT AREA  
TIN ON TAILS OVER NICKEL

**2. VOLTAGE RATING:**

125 VAC RMS

**3. CURRENT RATING:**

1.5 AMPS @25°C

**4. CONTACT RESISTANCE:**

20mΩ MAX

**5. INSULATOR RESISTANCE:**

1000 MΩ MIN @ 500V DC

**5. DIELECTRIC WITHSTANDING:**

1000V AC CONTACT TO CONTACT OR  
1500VDC CONTACT TO SCREEN AND TEST PANEL  
@0.5mA 50Hz OR 60Hz 60s

**6. DURABILITY:**

750 CYCLES MIN

**7. MATING/UNMATING FORCE:**

30N/3.06KG MAX

**8. RETENTION STRENGTH:**

30N MIN FOR 60s ±5s

**9. OPERATING TEMP:**

-40°C TO +85°C

**10. STORAGE TEMP:**

-40°C TO +85°C RELATIVE HUMIDITY <80%

MATES WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.



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**RJ45, w/LED, RIGHT ANGLE PCB MOUNT**

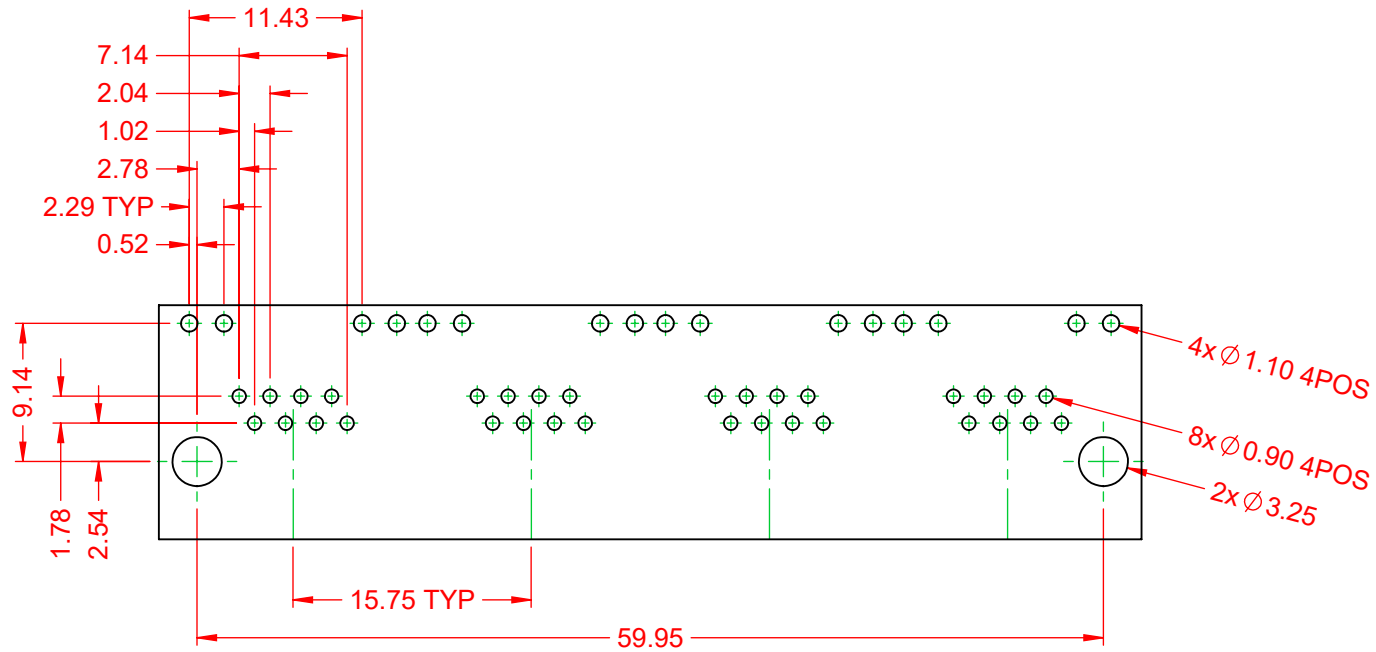
ACAD REFERENCE NO.: J67 Assembly	
DRAWN: C.B	DATE: January 16, 2020
CHECKED:	DATE:
SCALE: 2:1	SHEET 1 OF 2
DRAWING NUMBER: J67 Assembly	ISSUE
PART NUMBER: J67048821P61011	1

TOLERANCE UNLESS OTHERWISE SPECIFIED	
.X	±0.25
.XX	±0.13
.XXX	±0.05

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ISSUE NUMBER	
ORIGINAL	①



SUGGESTED PCB LAYOUT

<b>RJ45, w/LED, RIGHT ANGLE PCB MOUNT</b>	ACAD REFERENCE NO.: J67 Assembly	
	DRAWN: C.B	DATE: January 16, 2020
	CHECKED:	DATE:
	SCALE: 2:1	SHEET 2 OF 2
	DRAWING NUMBER: J67 Assembly	ISSUE
	PART NUMBER: J67048821P61011	1



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