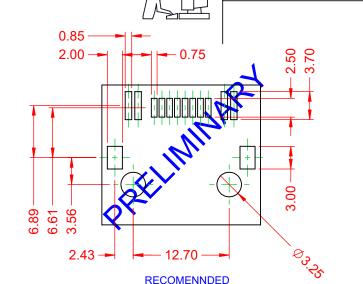


TOLERANCE

UNLESS OTHERWISE SPECIFIED

.X ±0.25

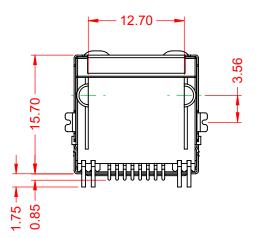
.XX ±0.13



PCB LAYOUT

ISSUE NUMBER

ORIGINAL



NOTES:

1. MATERIALS:

INSULATOR: LCP, BLACK, UL 94V-0

CONTACT: PHOSPHOR BRONZE, SELECT 50µ" GOLD PLATING ON CONTACT AREA

THIS IS A C.A.D. GENERATED DRAWING

DO NOT MAKE MANUAL REVISIONS TO MASTER.

TIN ON TAILS OVER NICKEL

VOLTAGE RATING:
 CURRENT RATING:
 AMPS @25°C
 CONTACT RESISTANCE:
 20mΩ MAX

5. INSULATOR RESISTANCE: 1000 MΩ MIN @ 500V DC

5. DIELECTRIC WITHSTANDING: 1000V AC CONTACT TO CONTACT

OR 1500VDC CONTACT TO SCREEN AND TEST PANEL

@0.5mA 50Hz OR 60Hz 1 MIN

6. DURABILITY: 750 CYCLES MIN 7. MATING/UNMATING FORCE: 30N/3.06KG MAX

8. RETENTION STRENGTH: 50N MIN FOR 60 S +15 S

9. OPERATING TEMP: -40°C TO +85°C

10. STORAGE TEMP: -40°C TO +85°C RELATIVE HUMIDITY <80%

11. WAVE SOLDERING TEMP: 260+15°C FOR 5~10 SECONDS

MATES WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.

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 ACAD REFERENCE NO.:
 J6D Assembly

 DRAWN:
 C.B

 DATE:
 January 13, 2020

 CHECKED:
 DATE:

 SCALE:
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DRAWING NUMBER: J6D Assembly ISSUE
PART NUMBER: J6D018894L16112 1

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